

Communication power supply







Overview

What is a multi-output power supply design?

Multiple output designs may also employ a complex regulation scheme which senses multiple outputs to control the feedback loop. Voice-over-Internet-Protocol (VoIP), Digital Subscriber Line (DSL), and Third-generation (3G) base stations all necessitate varying degrees of complexity in power supply design.

What voltage does a DSL power system supply?

The DSL power system may supply both higher voltage analog line drivers and amplifiers (typ. \pm 12V) and several low voltage supplies required by the digital ASIC (\pm 5V, \pm 3.3V, \pm 1.8V, \pm 1.5V).

What is a preferred power supply architecture for DSL applications?

A preferred power supply architecture for DSL applications is illustrated in Fig. 2. A push-pull converter is used to convert the 48V input voltage to +/-12V and to provide electrical isolation. Synchronous buck converters powered off of the +12V rail generate various low-voltage outputs.

What types of power systems are used in communications infrastructure equipment?

Communications infrastructure equipment employs a variety of power system components. Power factor corrected (PFC) AC/DC power supplies with load sharing and redundancy (N+1) at the front-end feed dense, high efficiency DC/DC modules and point-of-load converters on the back-end.

What is a low profile power supply?

Low profile power supply design usually includes printed circuit board (planar) power transformers and output inductors and surface mount input and output capacitors. Multiple output power supplies are often implemented with a multi-output flyback converter.



What are hybrid isolated power supply topologies?

Competing with these new POL modules are hybrid isolated power supply topologies, such as the cascaded current-fed or voltage-fed push-pull converters. Semiconductor suppliers are enabling power supply system designers to embed low-cost compact isolated power supplies directly onto their motherboards and line cards.



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